

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|   |                  |
|---|------------------|
| Applicant: Yasunobu TAGUSA  | Art Unit: 1792   |
| Application No.: 10/598,488   |                  |
| Confirmation No.: 6247  | Examiner: G. Rao |
| Filed: August 31, 2006  |                  |
| Title: METHOD FOR FABRICATING A<br>SEMICONDUCTOR DEVICE AND APPARATUS<br>FOR INSPECTING A SEMICONDUCTOR |                  |

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 28, 2009, please amend the above-identified application as follows:

☐ **Amendments to the Specification** begin on page      of this paper.

☒ **Amendments to the Claims** are reflected in the listing of the claims which begins on page 2 of this paper.

☐ **Amendments to the Drawings** begin on page      of this paper and include an attached replacement sheet. An **Appendix** including the amended drawing figures is attached following page      of this paper.

☒ **Remarks/Arguments** begin on page 5 of this paper.

Please note, if a box is not checked, then no corresponding amendment is being made.